

1/17

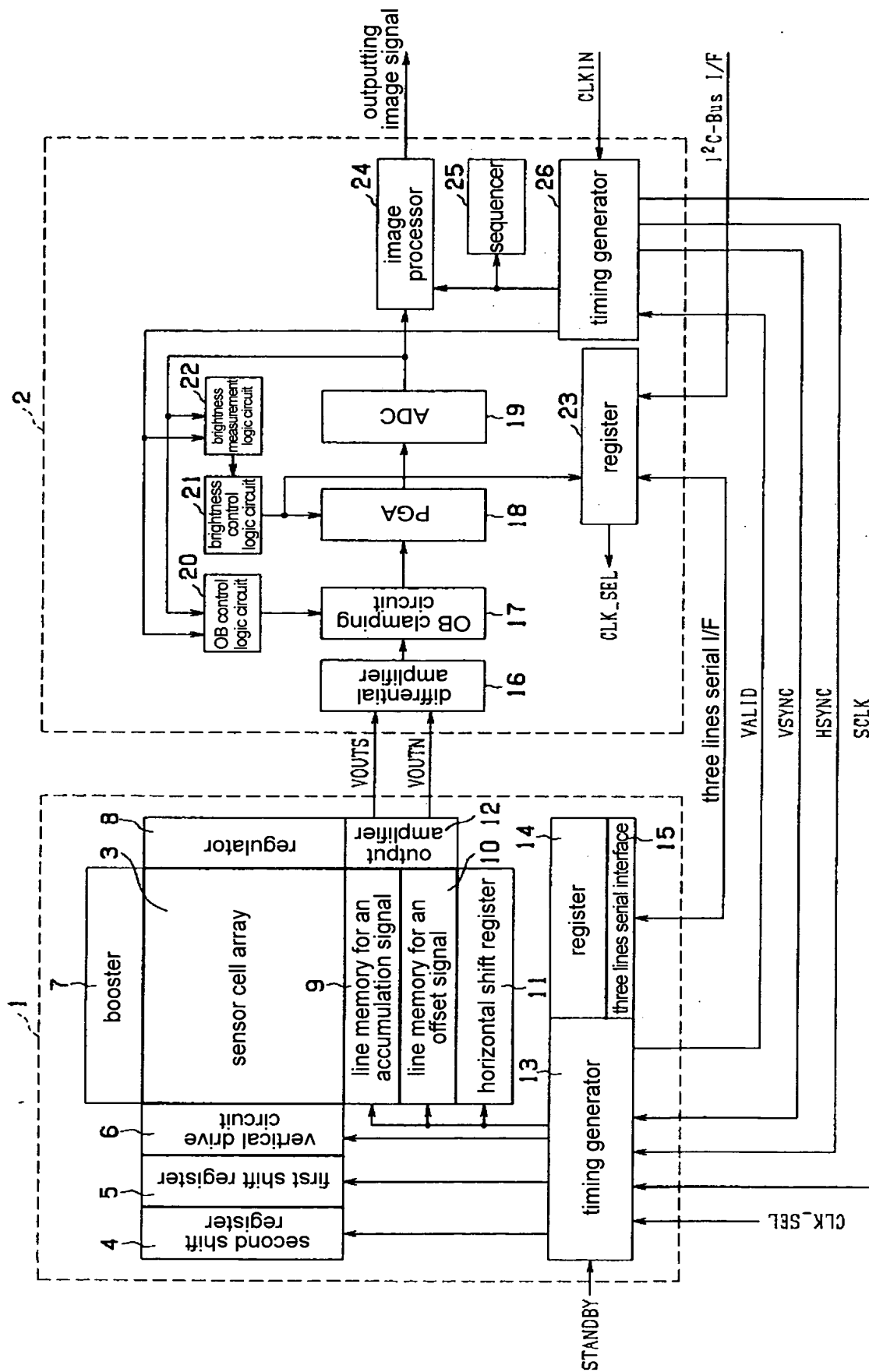


FIG. 1

2/17

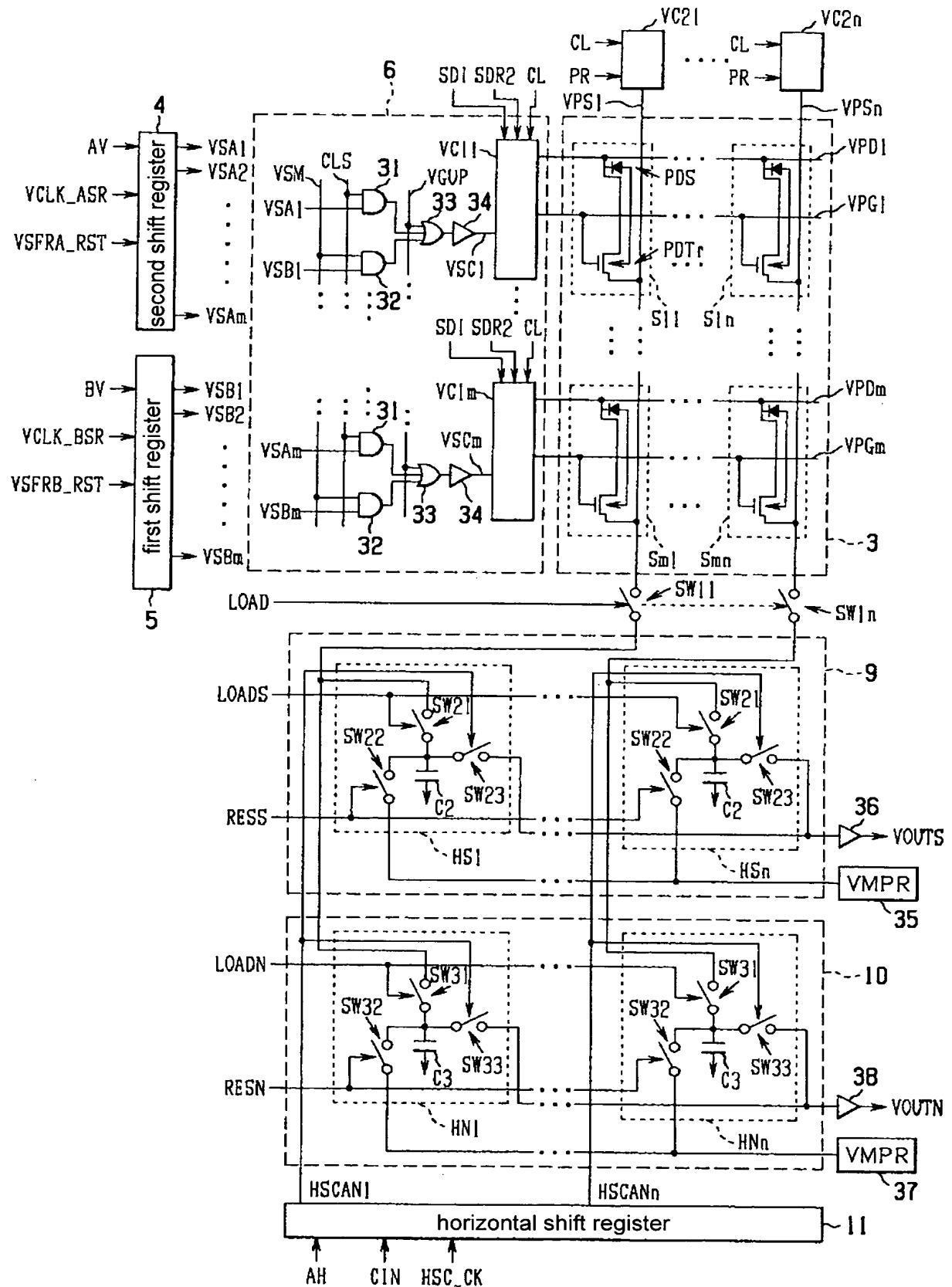
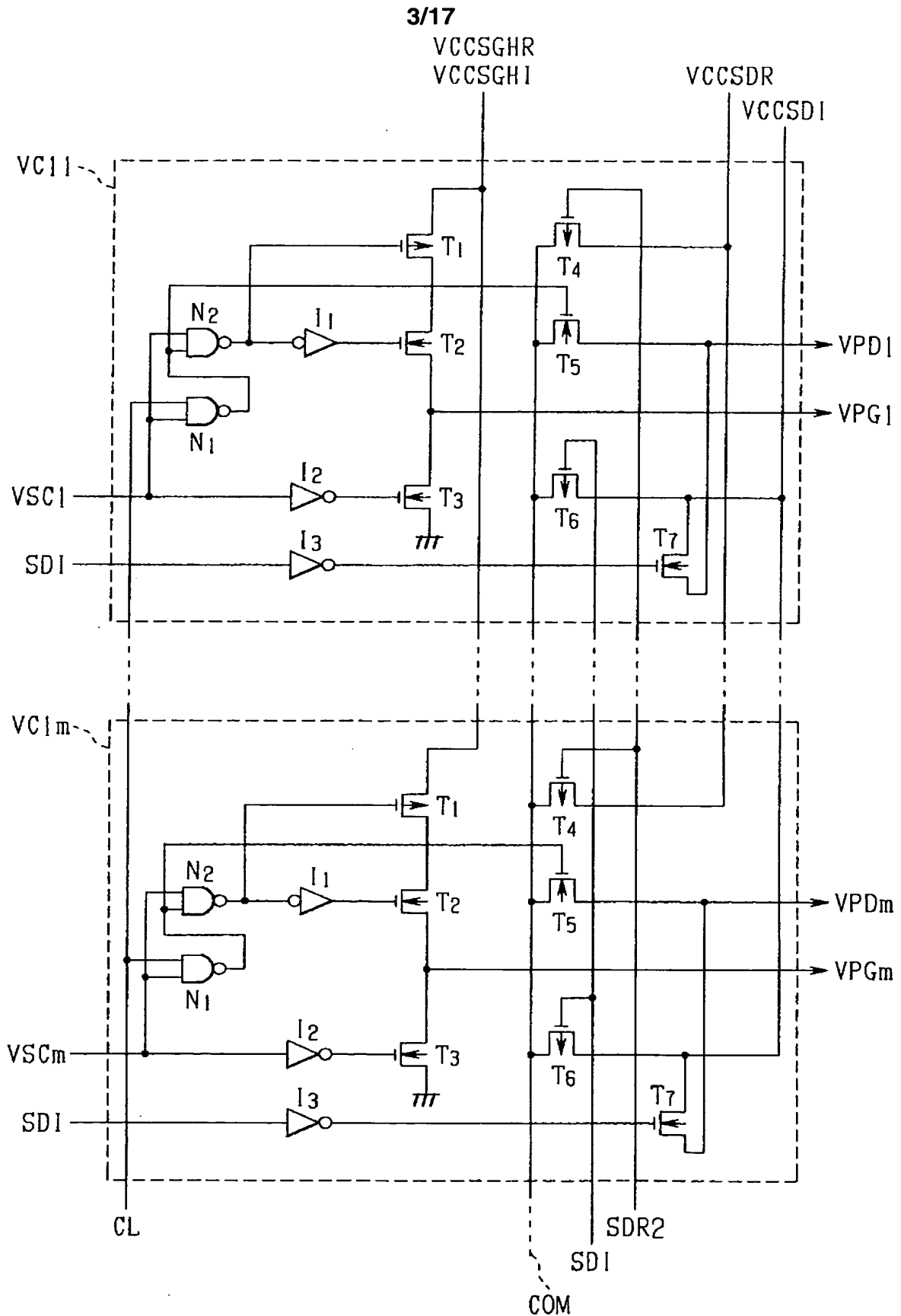


FIG. 2



**FIG. 3**

4/17

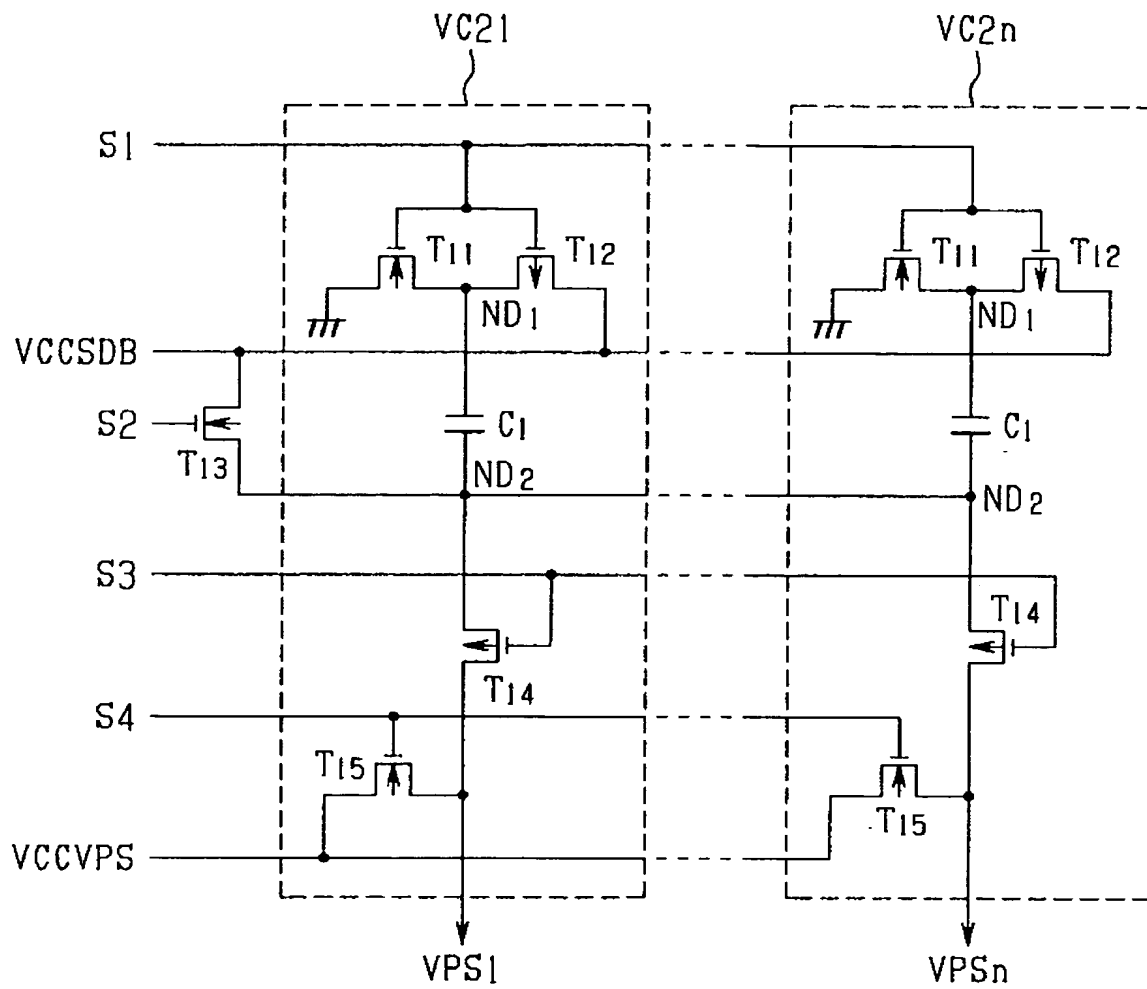


FIG. 4A

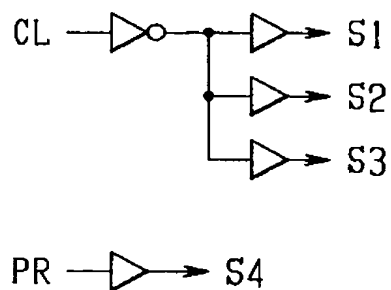


FIG. 4B

5/17

States of sensor bias

	state	accumulation	RESS	LOADS	PR	CL	RESN	LOADN
GATE	selected	VCCSGHI	Lo	VCCSGHR	VCCSGHR	VCCSGHR+ VCCSDBX 2	LoI	VCCSGHR
	non selected		Lo	Lo	Lo	Lo	Lo	Lo
SOURCE	selected	turning "on" (VCCSDI)	VMPR	VCCSGHR-VthS	VCCVPS	VCCSDBX 2	VMPR	VCCSGHR-VthN
DRAIN	selected	VCCSDI	HiZ	VCCSDR	turning "on" (VCCVPS)	turning "on" (VCCSDBX 2)	HiZ	VCCSDR
	non selected		HiZ	VCCSDR	VCCVPS	VCCSDR	HiZ	VCCSDR

FIG. 5

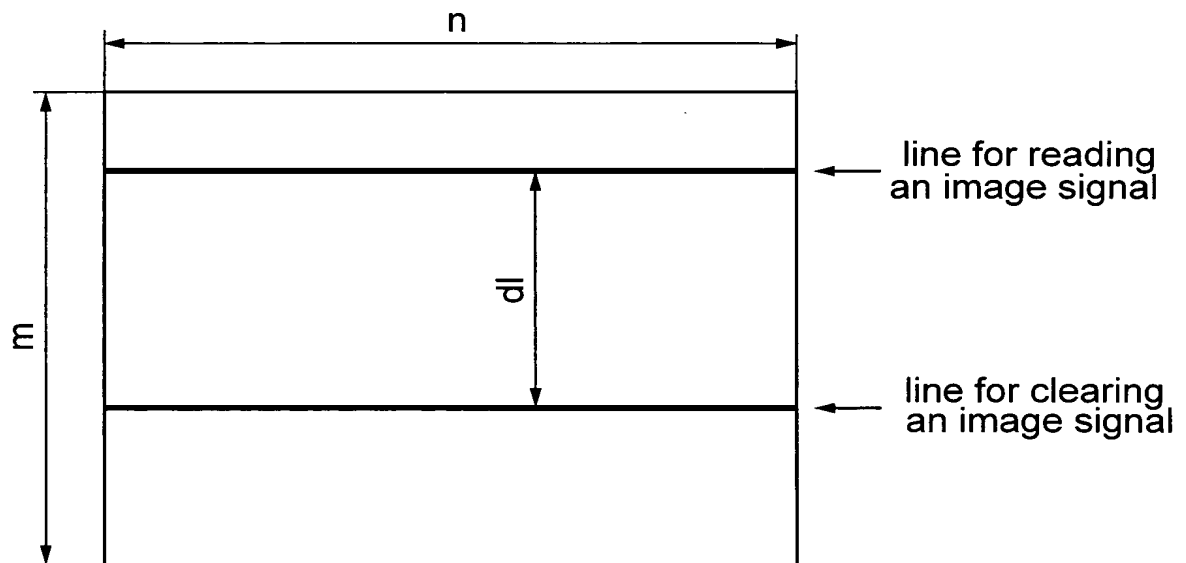


FIG. 6

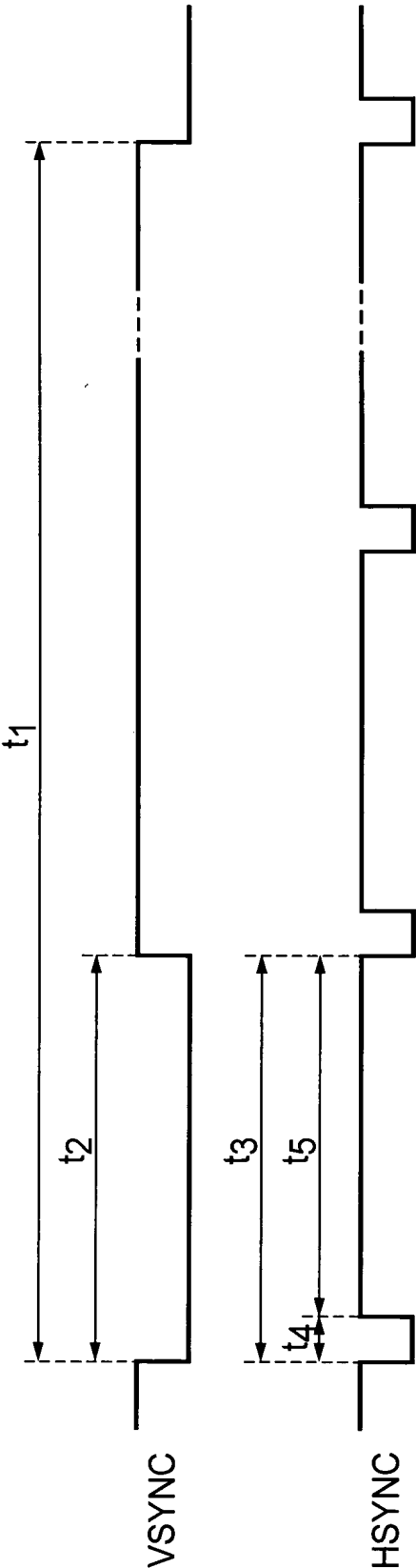


FIG. 7

8/17

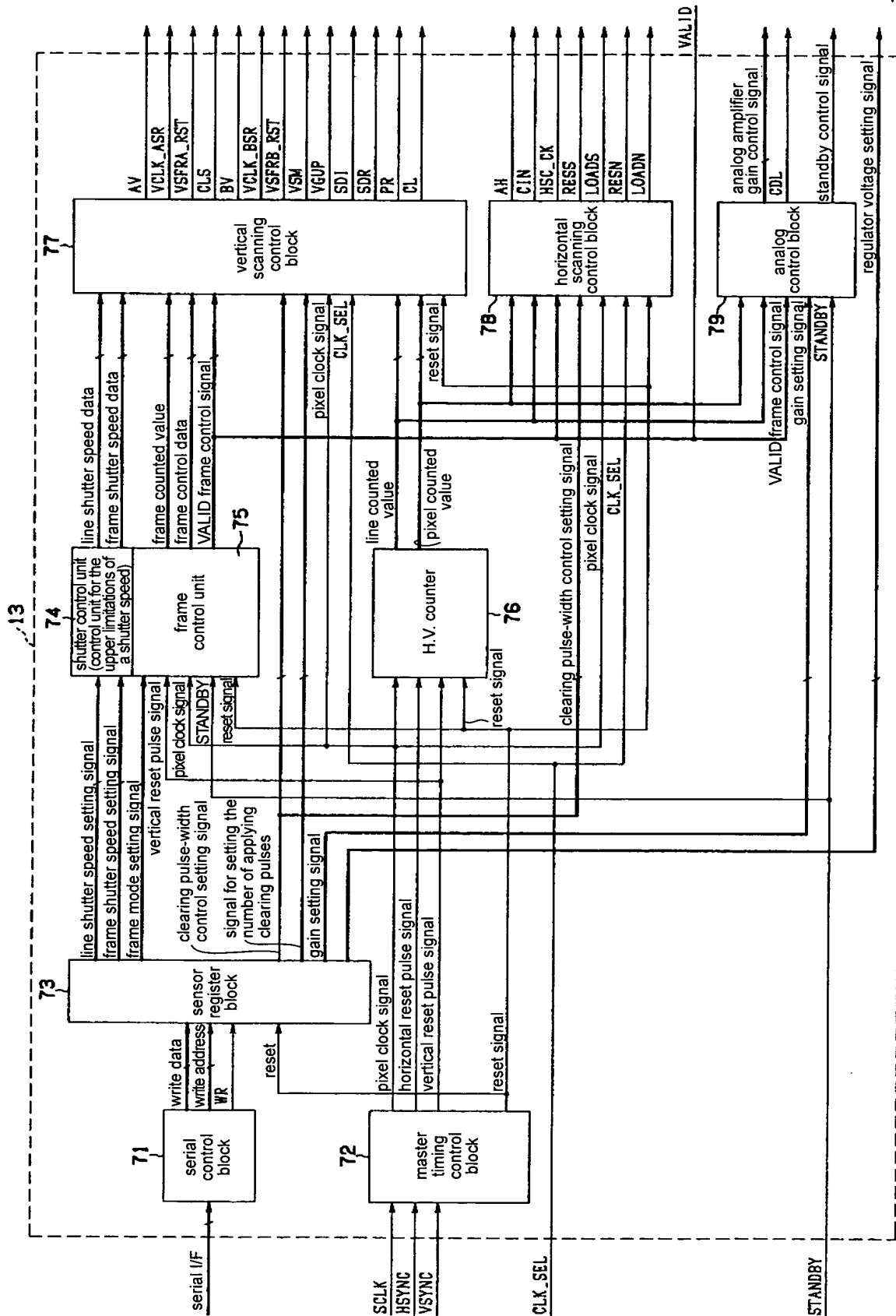


FIG. 8



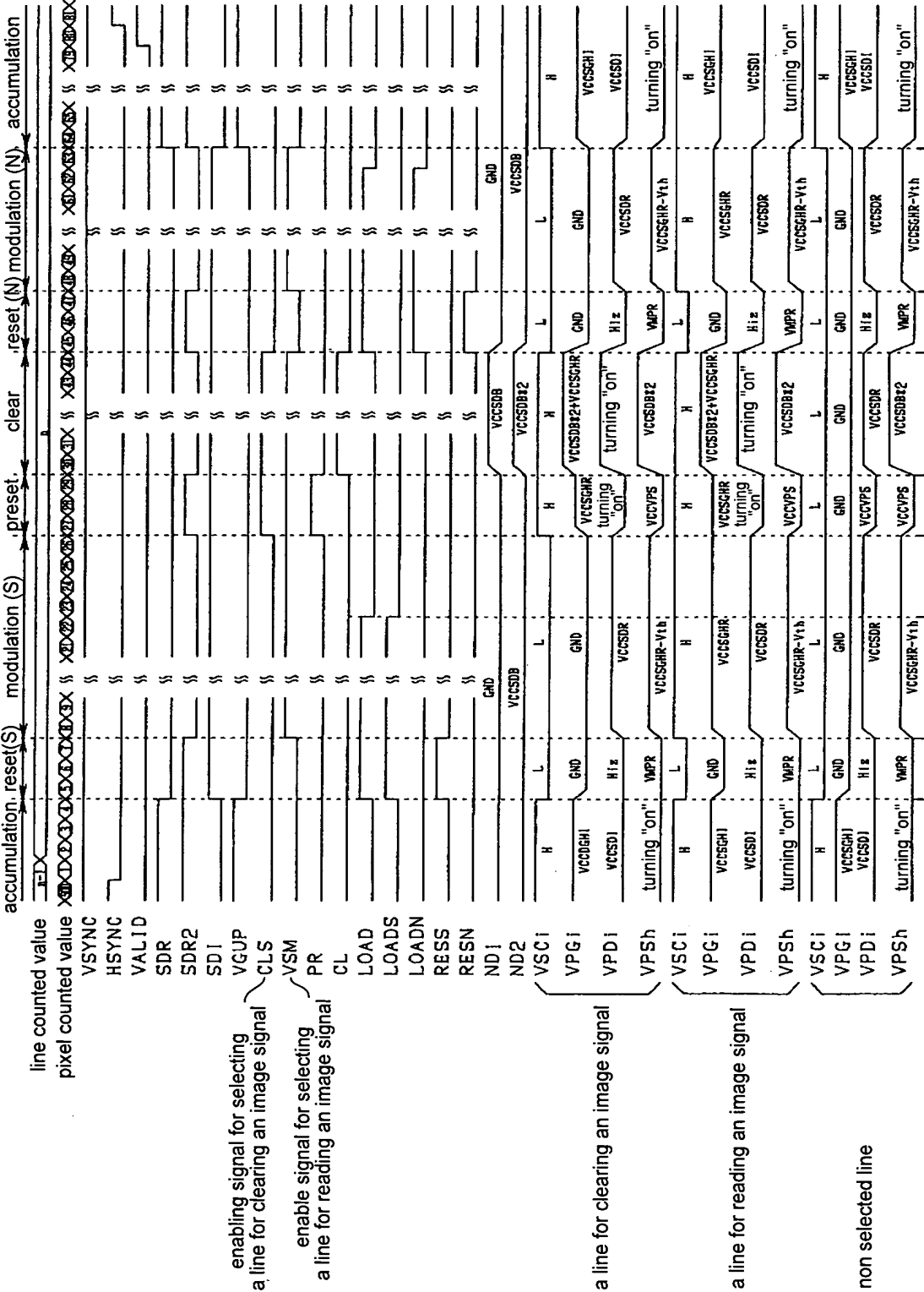


FIG. 9

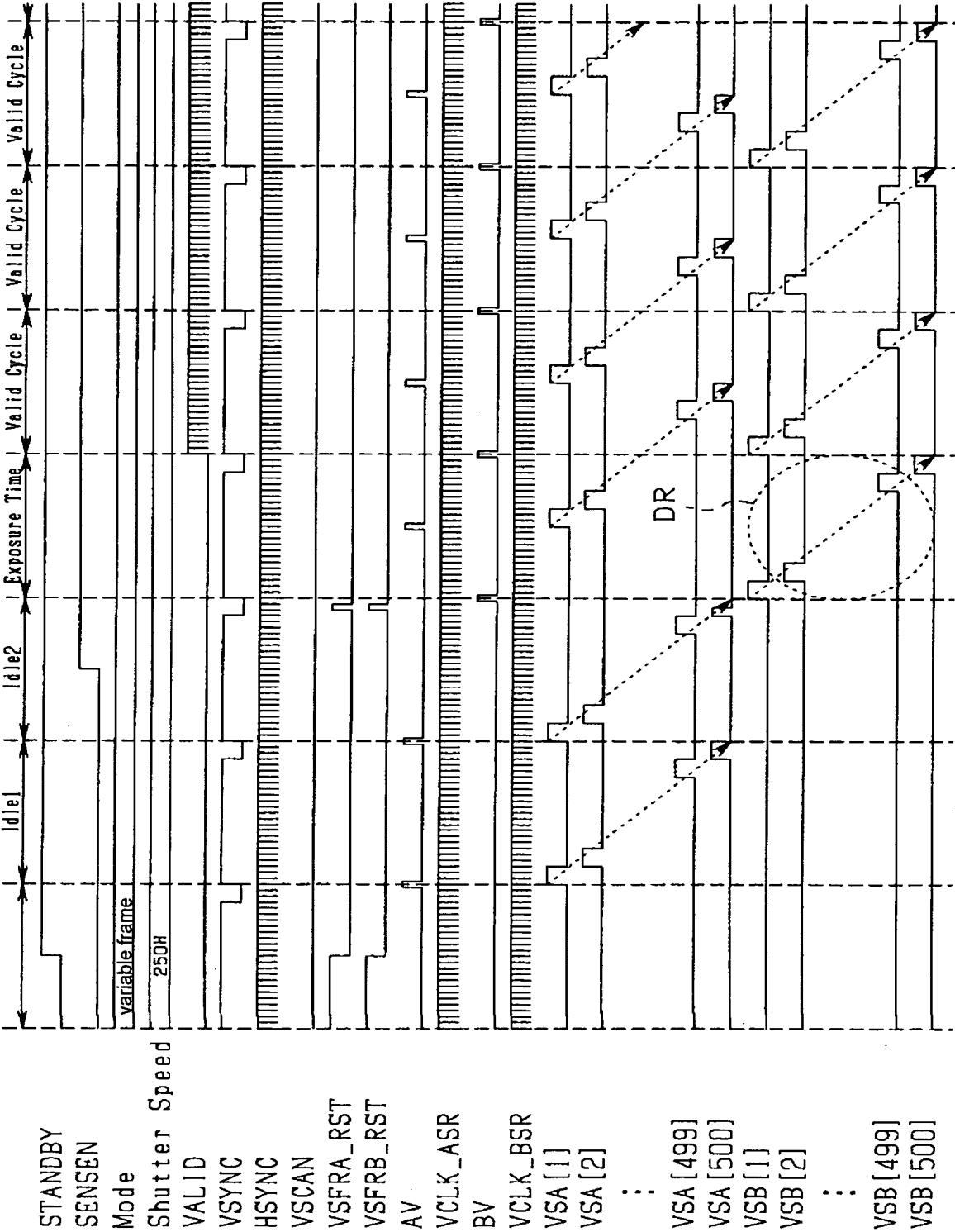


FIG. 10

11/17

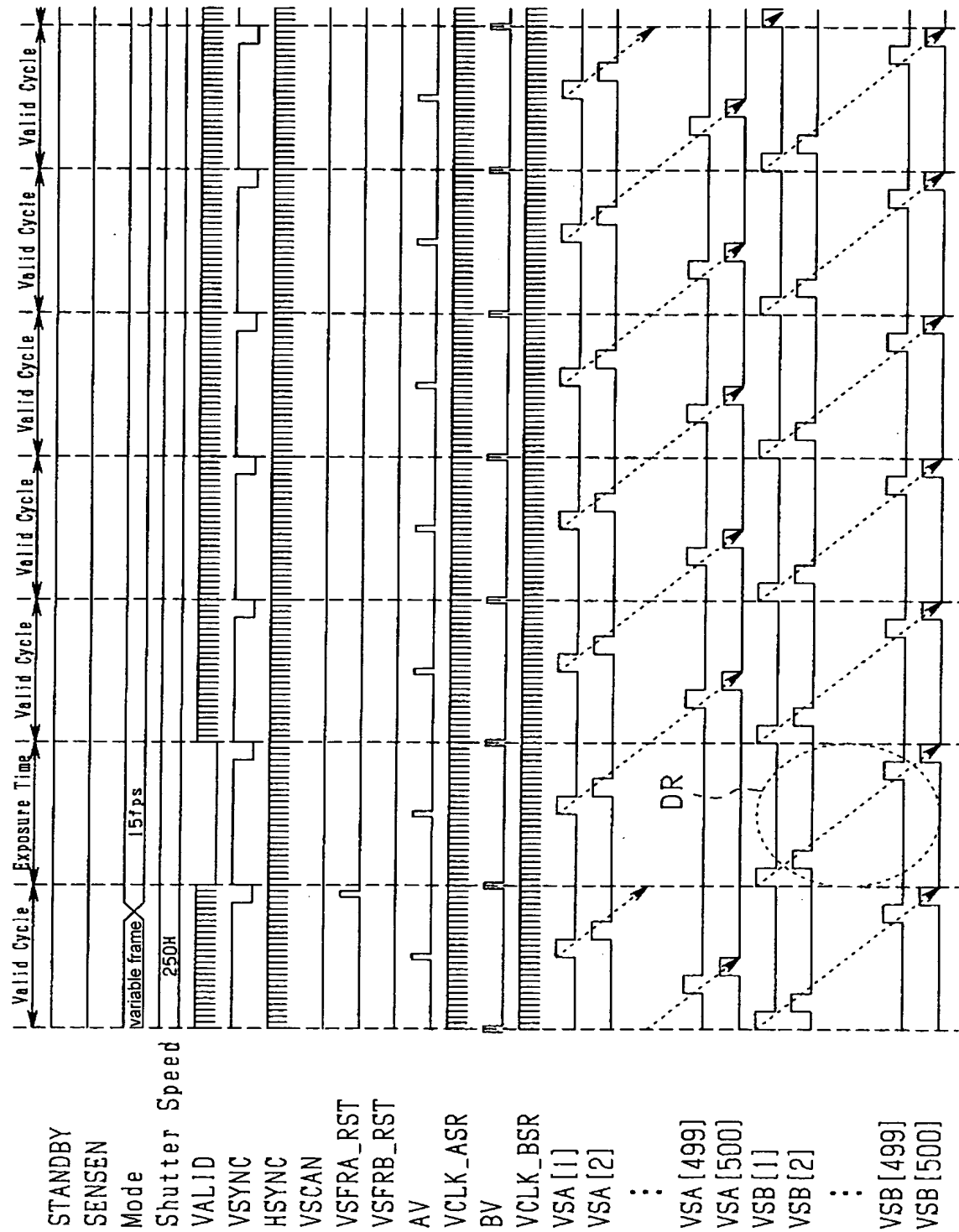


FIG. 11

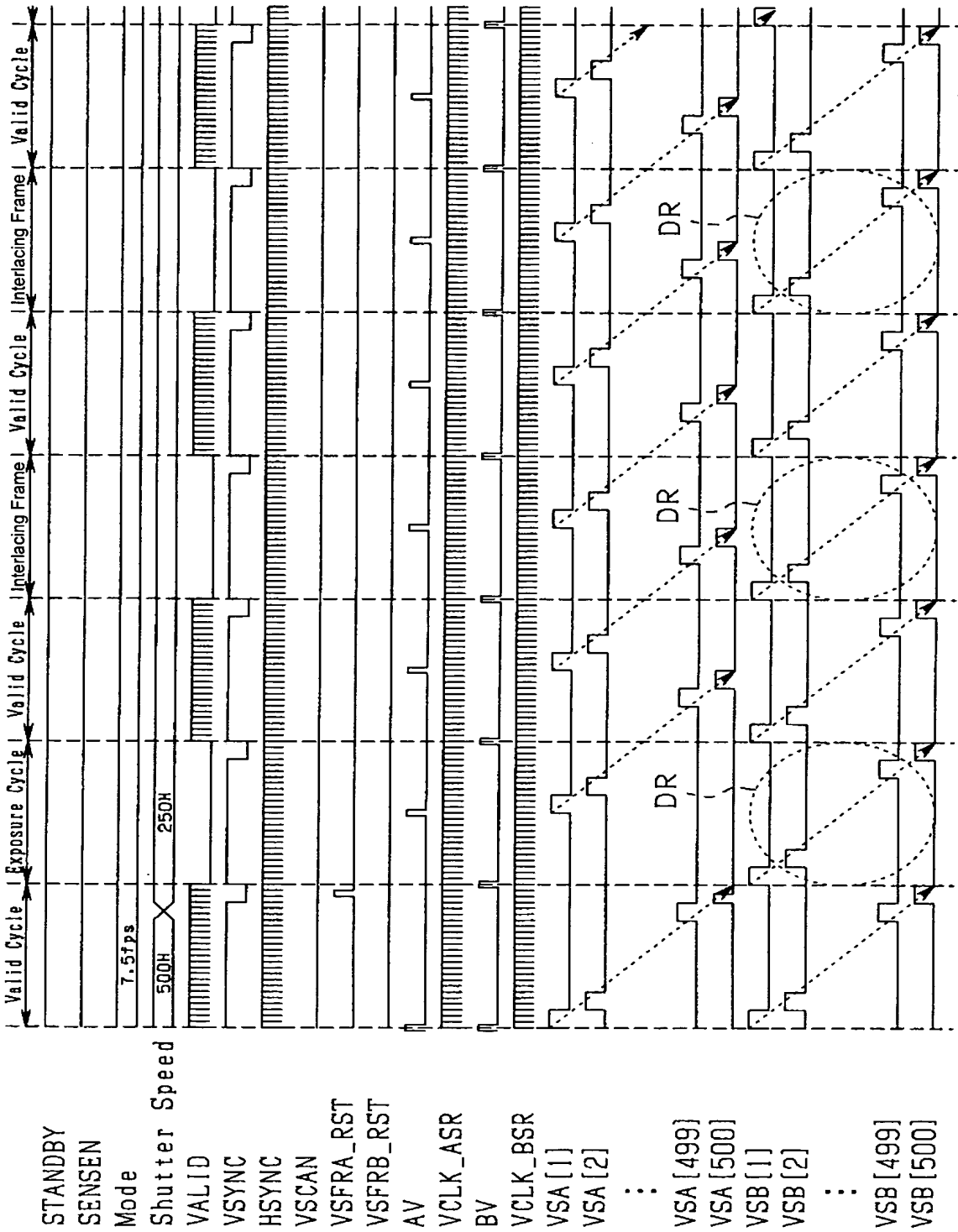


FIG. 12

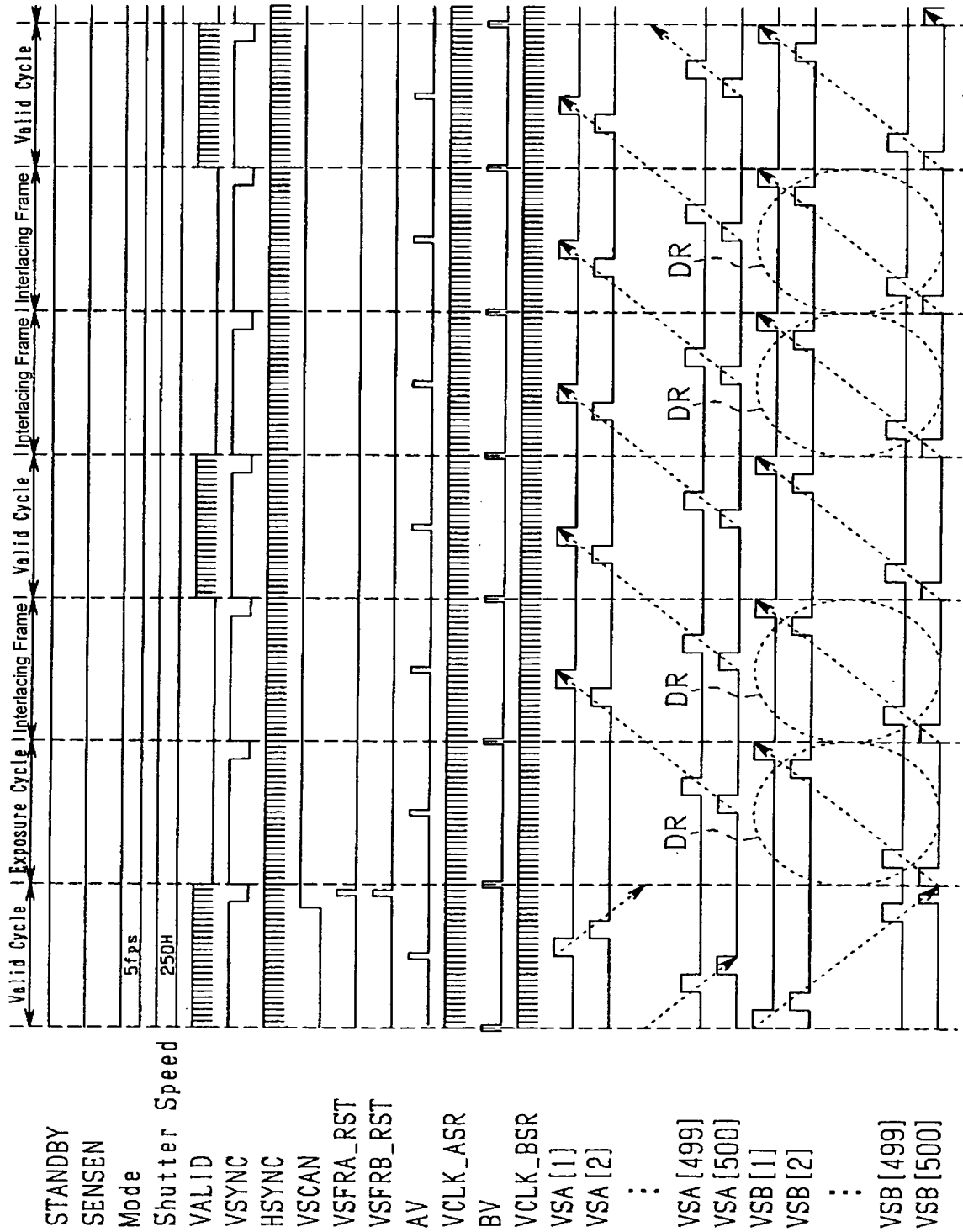


FIG. 13

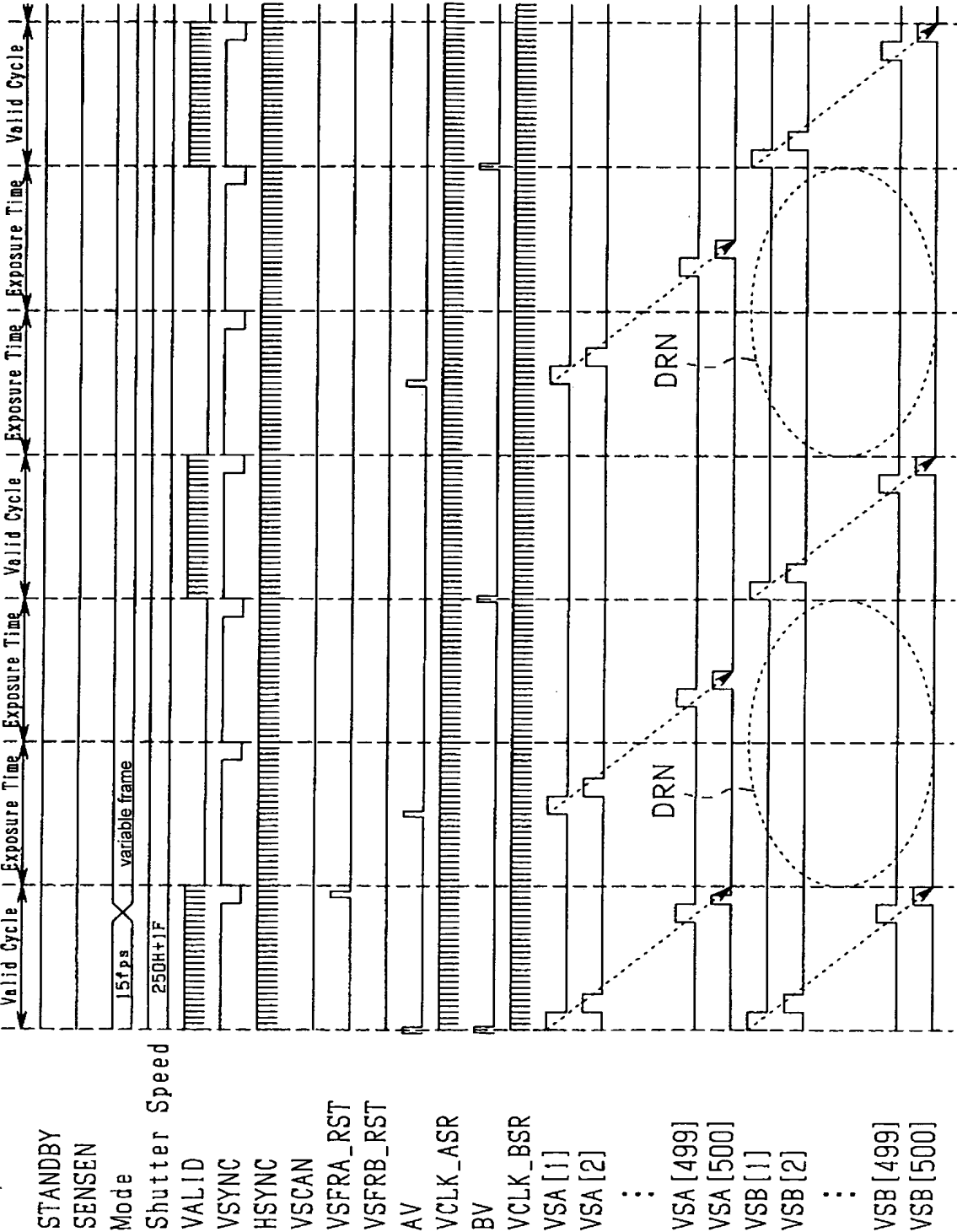


FIG. 14

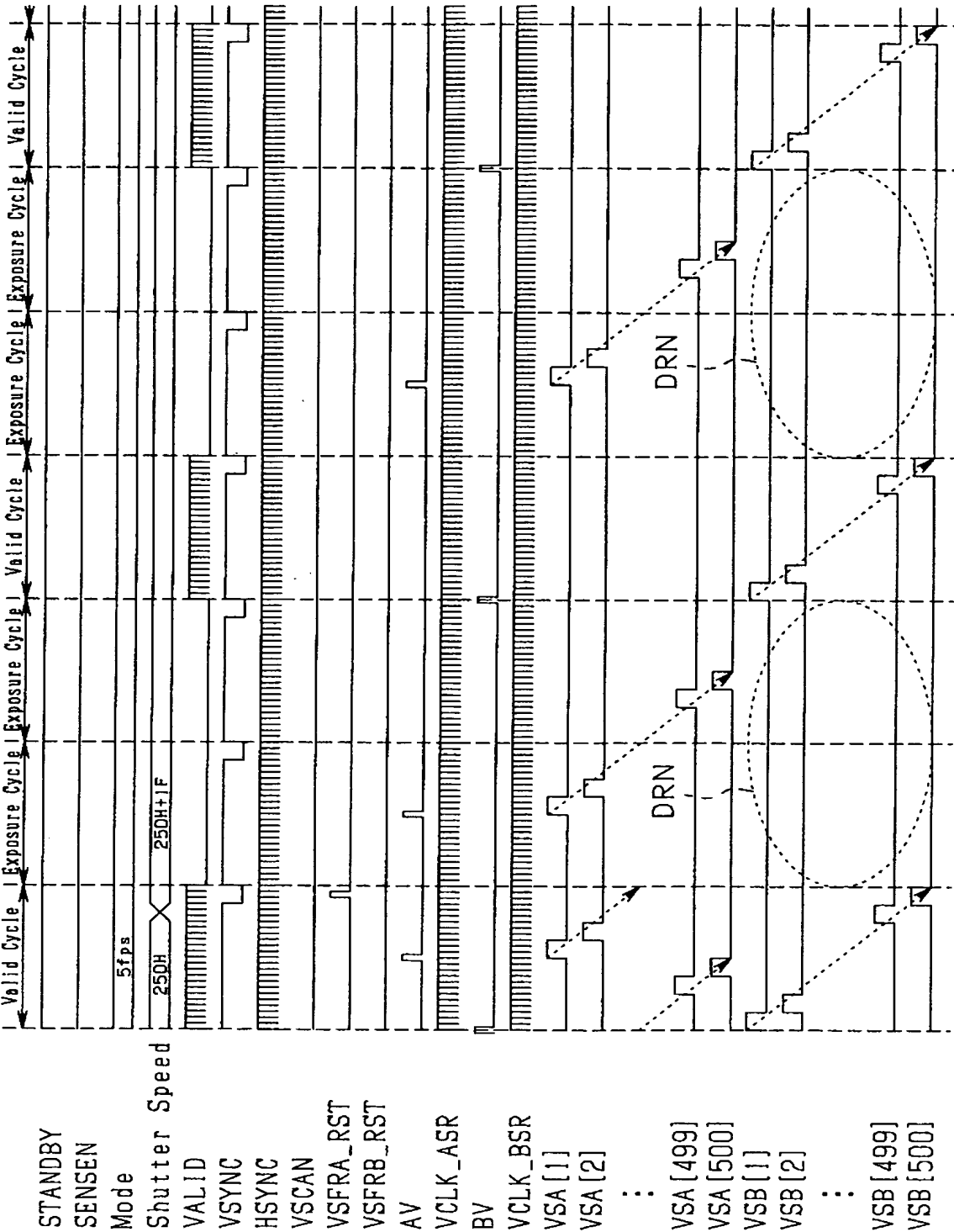


FIG. 15

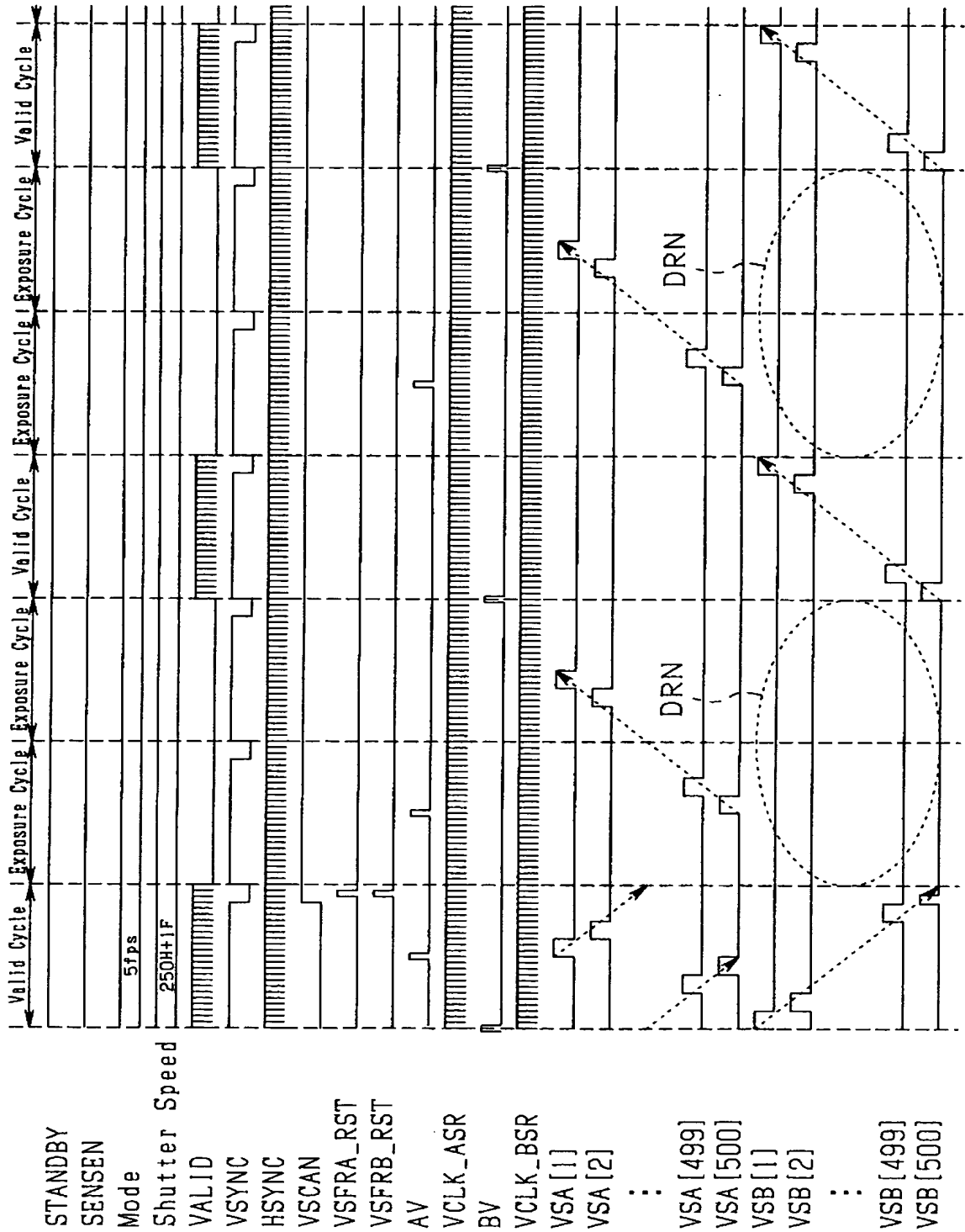


FIG. 16



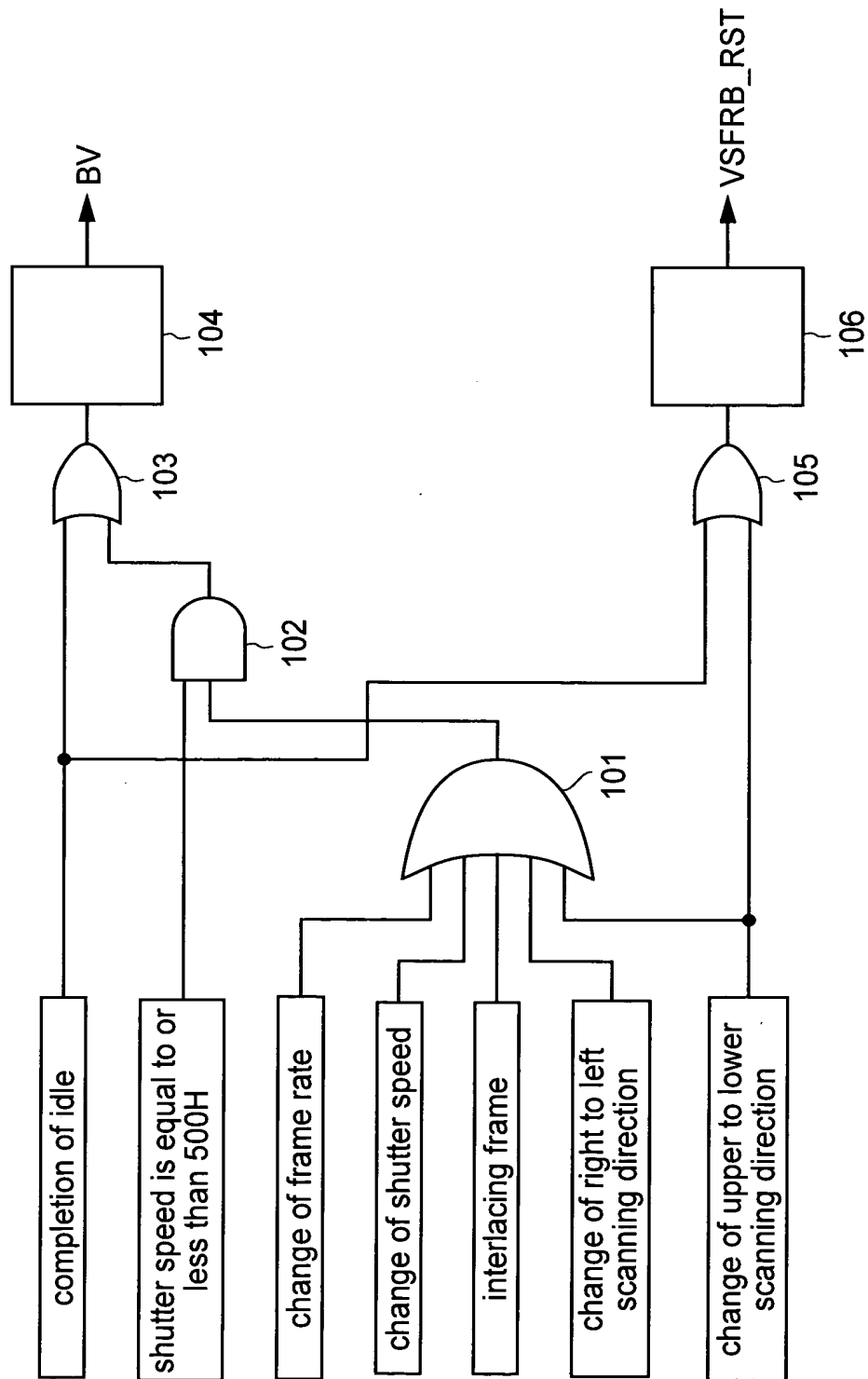


FIG. 17